

METHOD AND CIRCUIT FOR TESTING A REGULATED POWER SUPPLY IN AN INTEGRATED CIRCUIT

Abstract

A voltage regulated power supply test circuit including: a voltage regulator electrically connected to at least one regulated voltage node of a functional circuit of an integrated circuit chip; and means for selectively connecting between one of the at least one regulated voltage nodes and ground with at least one load circuit adapted to put an emulated current load of the functional circuit on the regulated voltage supply.